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25962 7590 09/23/2004

SLATER & MATSIL, L.L.P.  
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12/23/2004 JADD02 00000032 501065 09659872

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Kristin R. Hayes

(Depositor's name)

*KHayes*

(Signature)

December 22, 2004

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/659,872	09/13/2000	Hartmund Terletzki	00P7882US	7001

TITLE OF INVENTION: LEVEL-SHIFTING CIRCUITRY HAVING "HIGH" OUTPUT IMPEDANCE DURING DISABLE MODE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	<del>\$1930</del> 1400	\$0	<del>\$1930</del> 1400	12/23/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, MINH T	2816	327-333000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☒ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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1 Slater &amp; Matsil, L.L.P.

2

3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☒ Issue Fee☐ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies \_\_\_\_\_

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date 12/22/2004

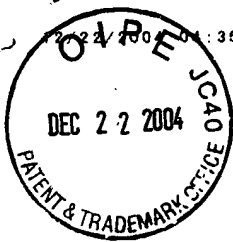
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**FACSIMILE COVER SHEET**

To Commissioner for Patents	Total Pages Sent: 4
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (703) 746-4000	Transmission Date: December 22, 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Terletzki, *et al.* Docket No.: 00P7882US  
Serial No: 09/659,872 Art Unit: 2816  
Date Filed: September 13, 2000  
Title: Level-Shifting Circuitry Having "High" Output Impedance During Disable Mode

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at (703) 746-4000 on the date shown above:

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- Part B - Fee Transmittal (1 original and 1 copy)
- Change of Correspondence Address Form (1 page)

Respectfully submitted,

  
Kristin Hayes  
Legal Assistant

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